

FIG. 1

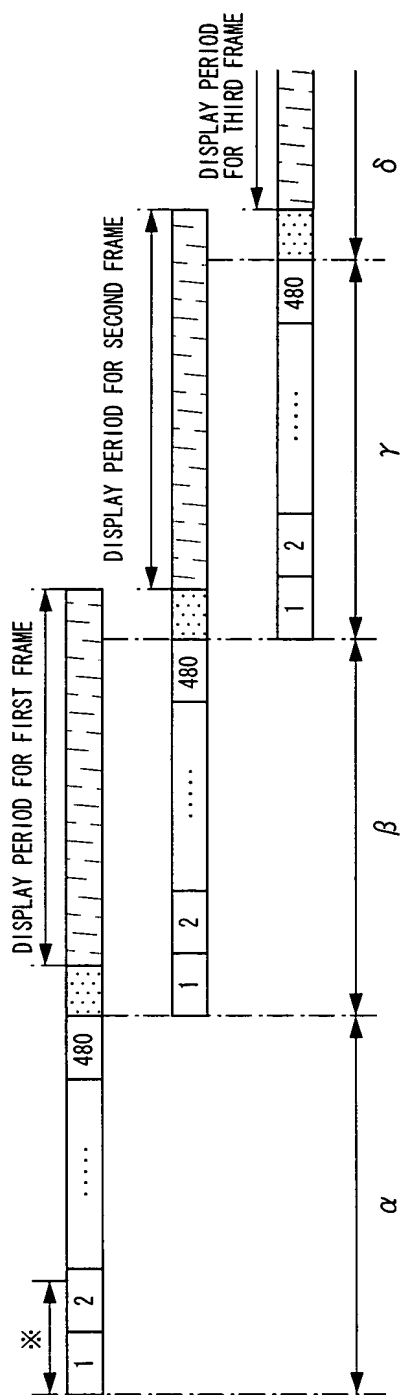


FIG. 3A

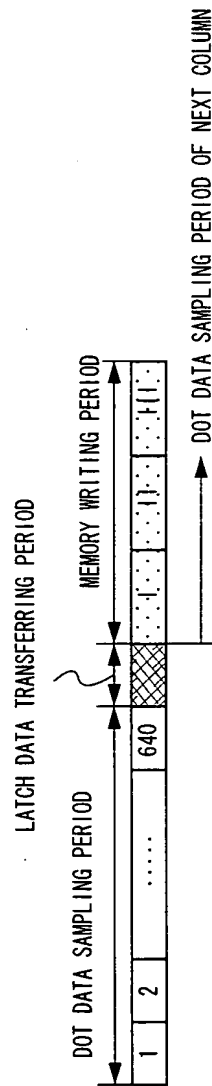


FIG. 3B

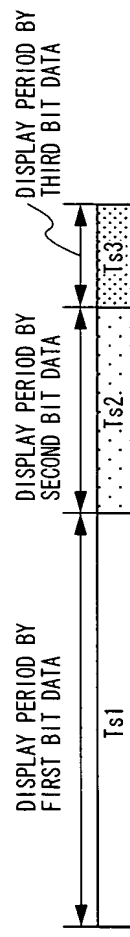


FIG. 3C

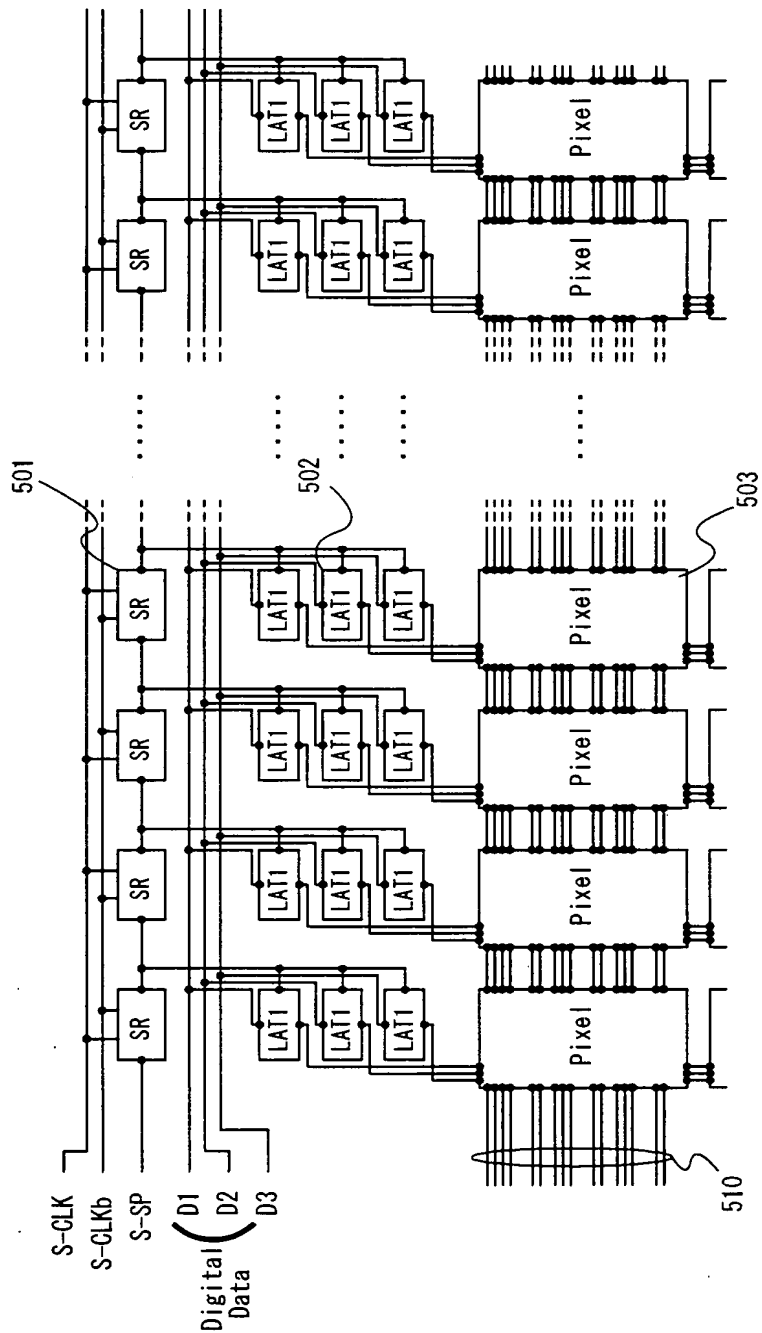


FIG. 5

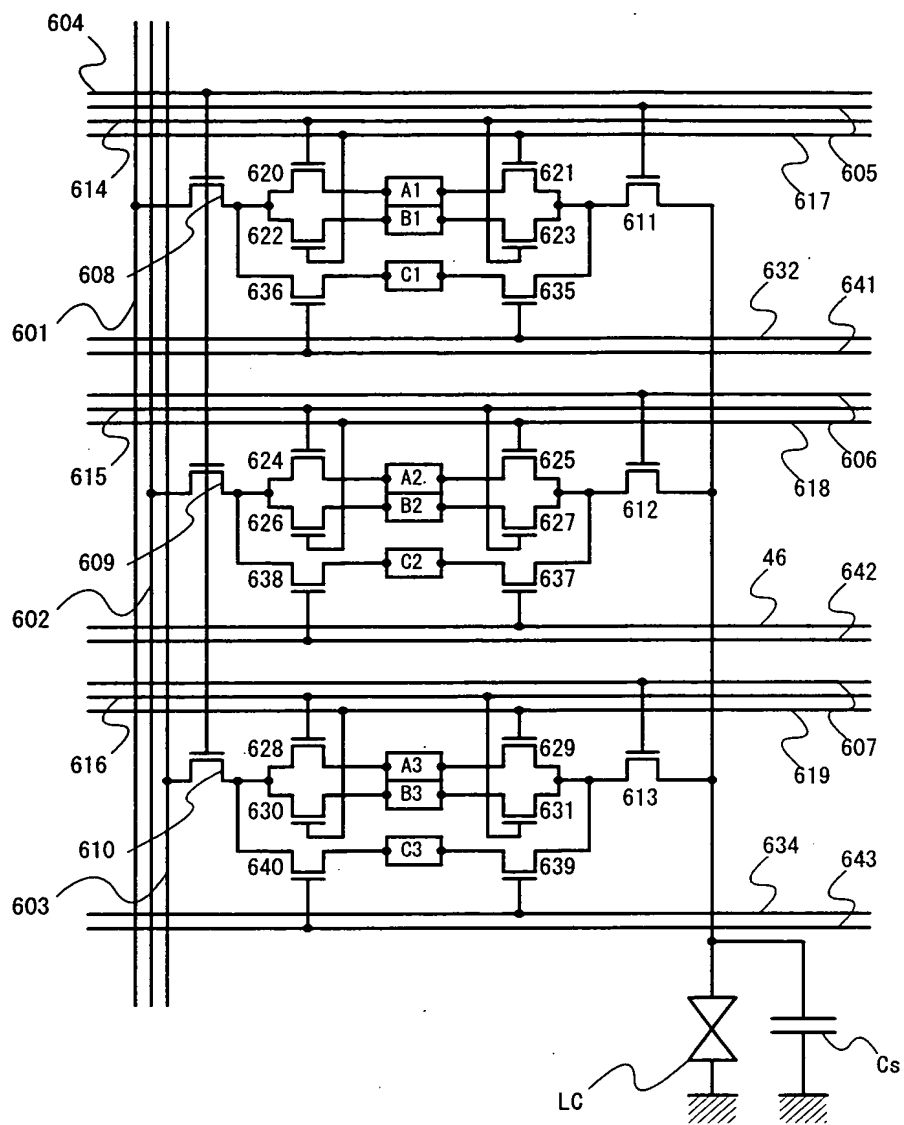


FIG. 6

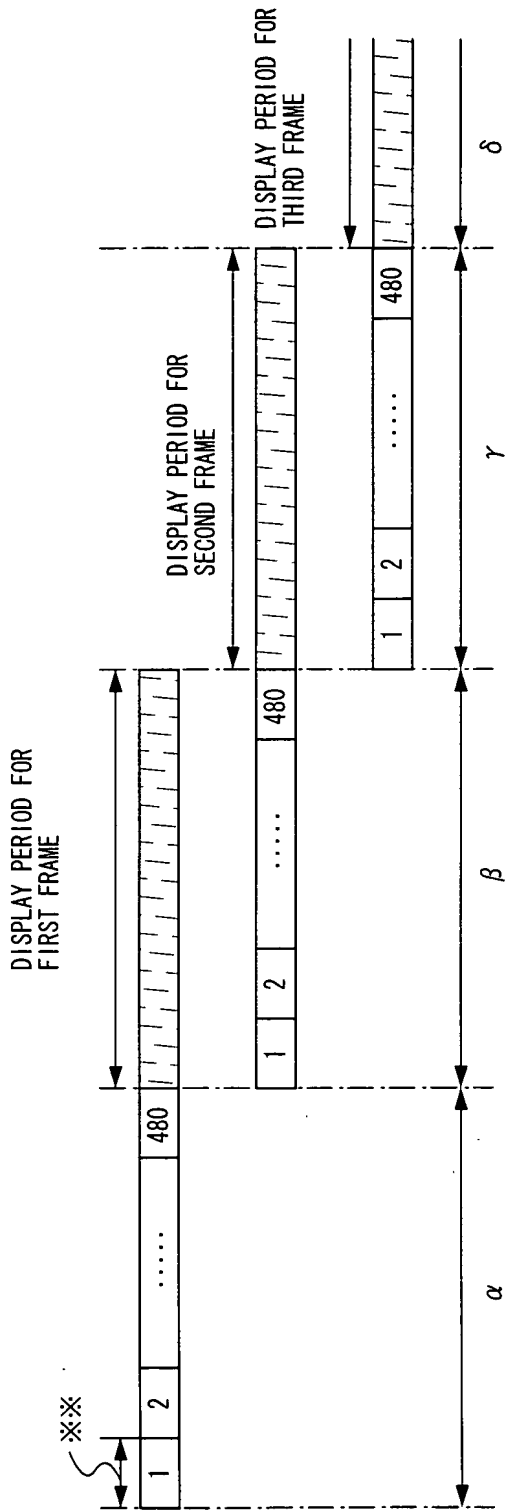


FIG. 7A



FIG. 7B

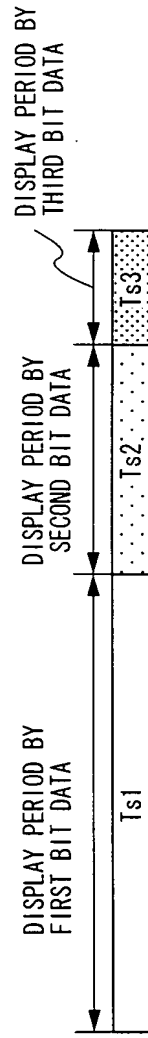


FIG. 7C

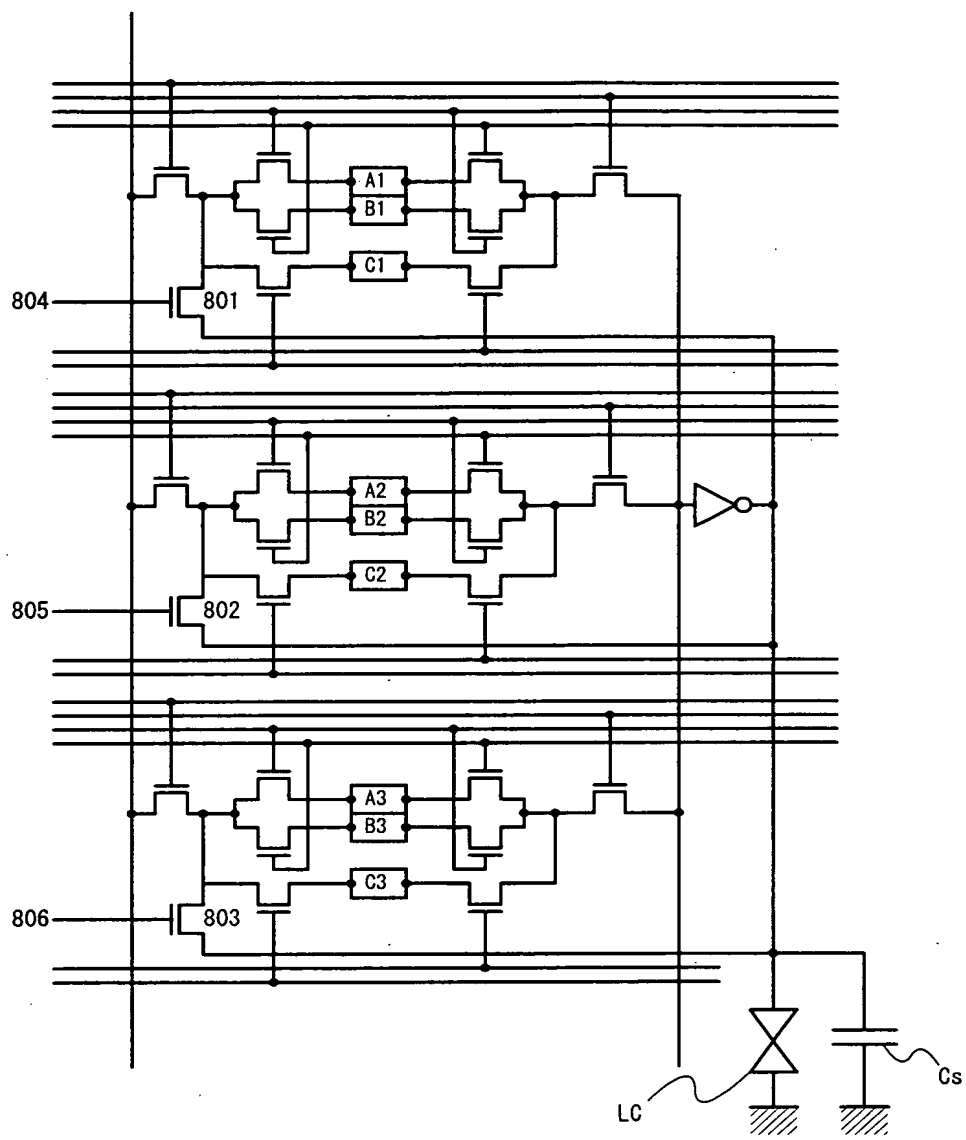


FIG. 8

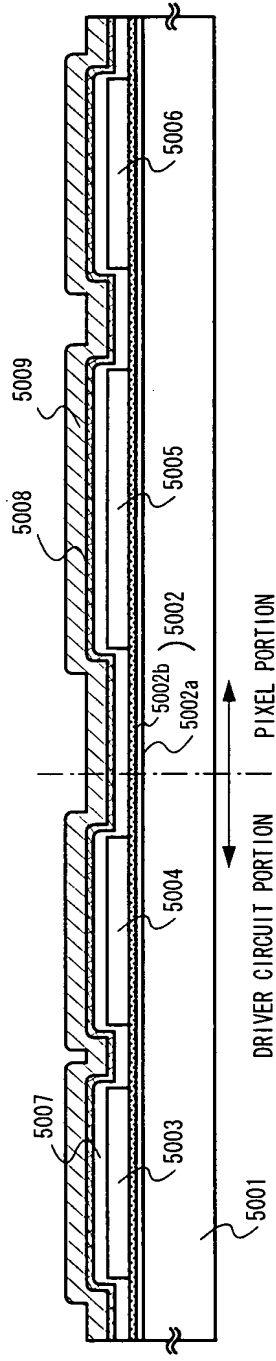


FIG. 9A

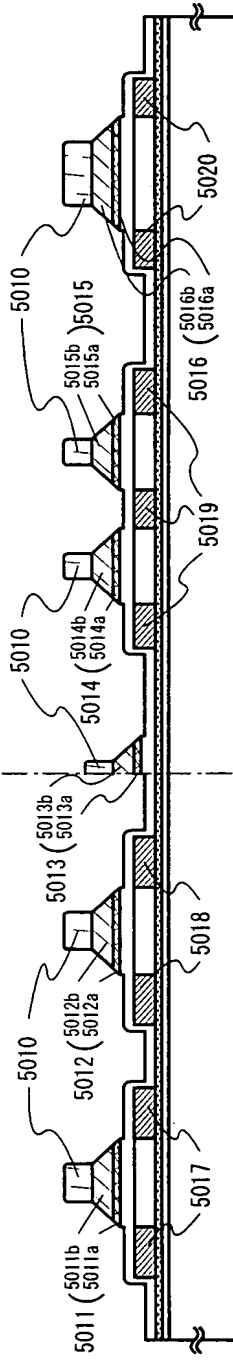


FIG. 9B

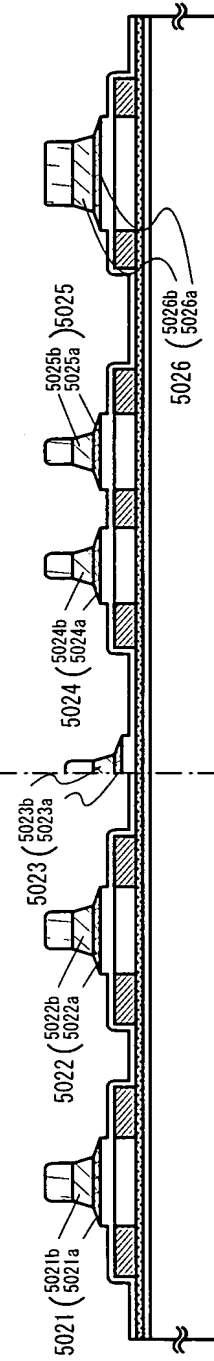


FIG. 9C

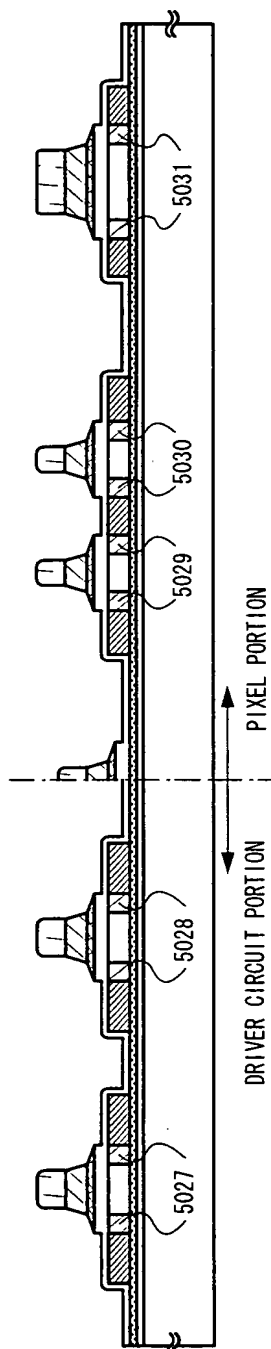


FIG. 10A

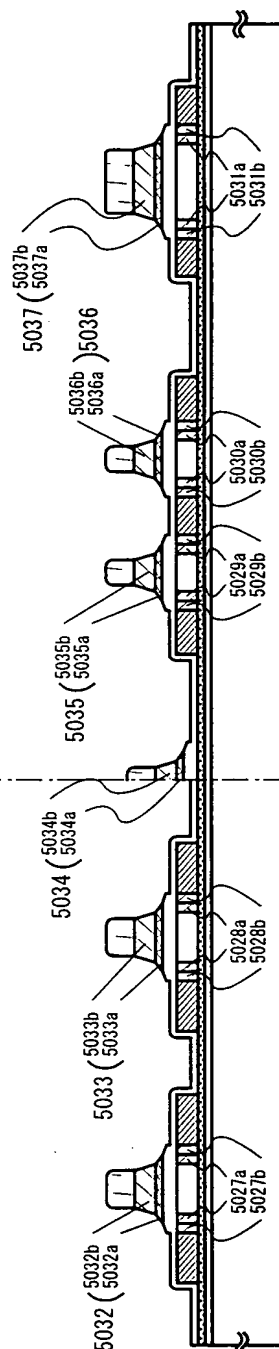


FIG. 10B

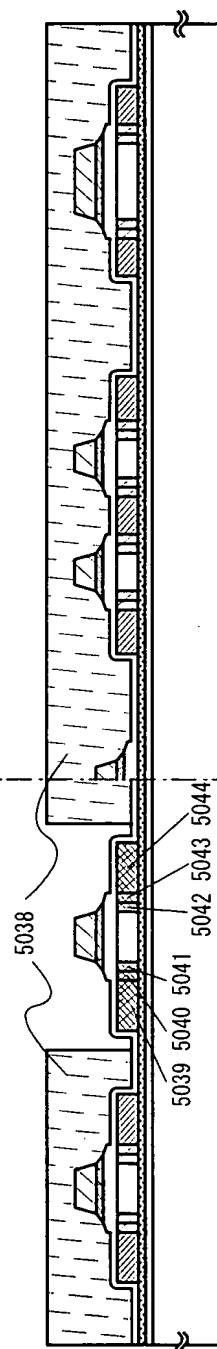


FIG. 10C

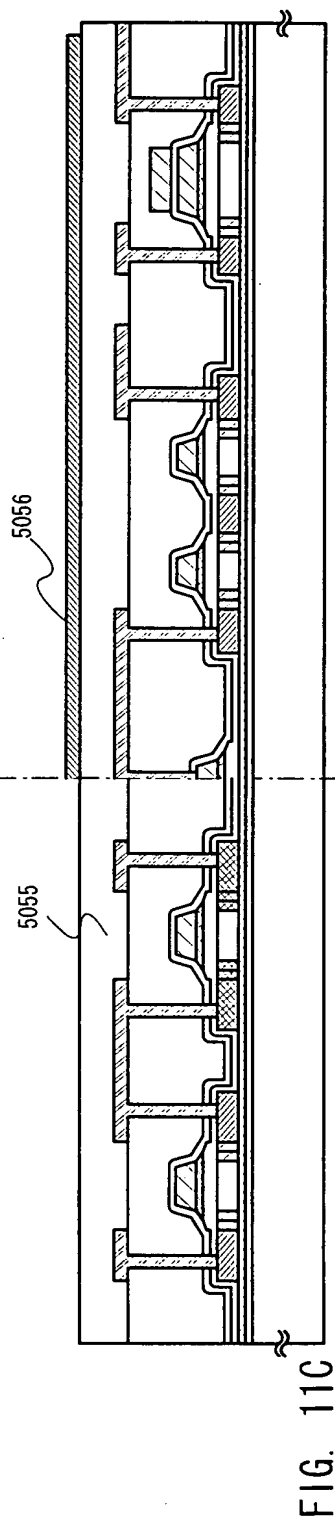
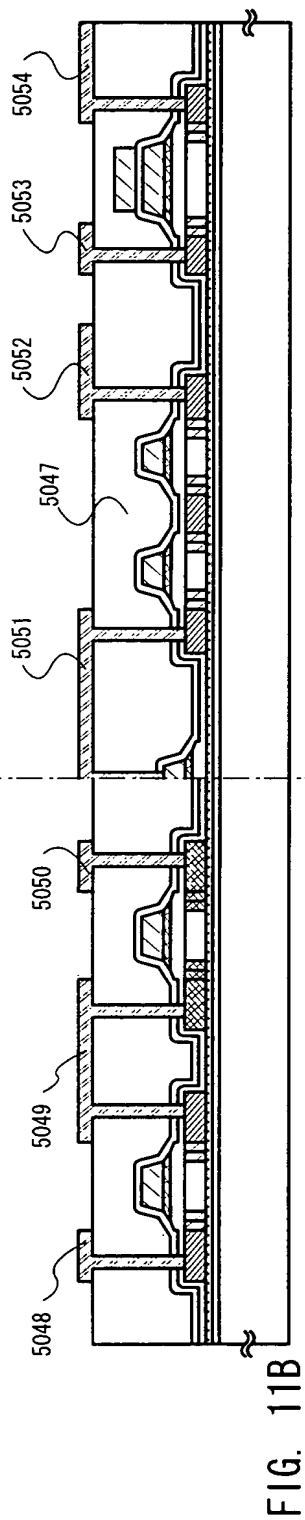
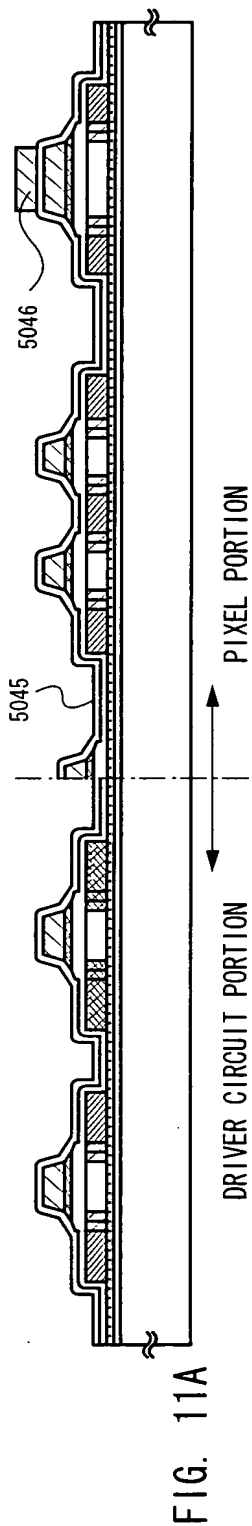


FIG. 12 is a cross-sectional view of a pixel portion of a display device, showing a driver circuit portion and a pixel portion. The driver circuit portion includes a first TFT (5061), a second TFT (5062), and a third TFT (5060). The pixel portion includes a pixel TFT (N) and a memory TFT. The cross-section shows various layers and structures, including a substrate, a gate insulating layer, and a pixel electrode. The labels 5061, 5062, 5060, 5057, 5059, and 5058 indicate specific regions or layers within the device.

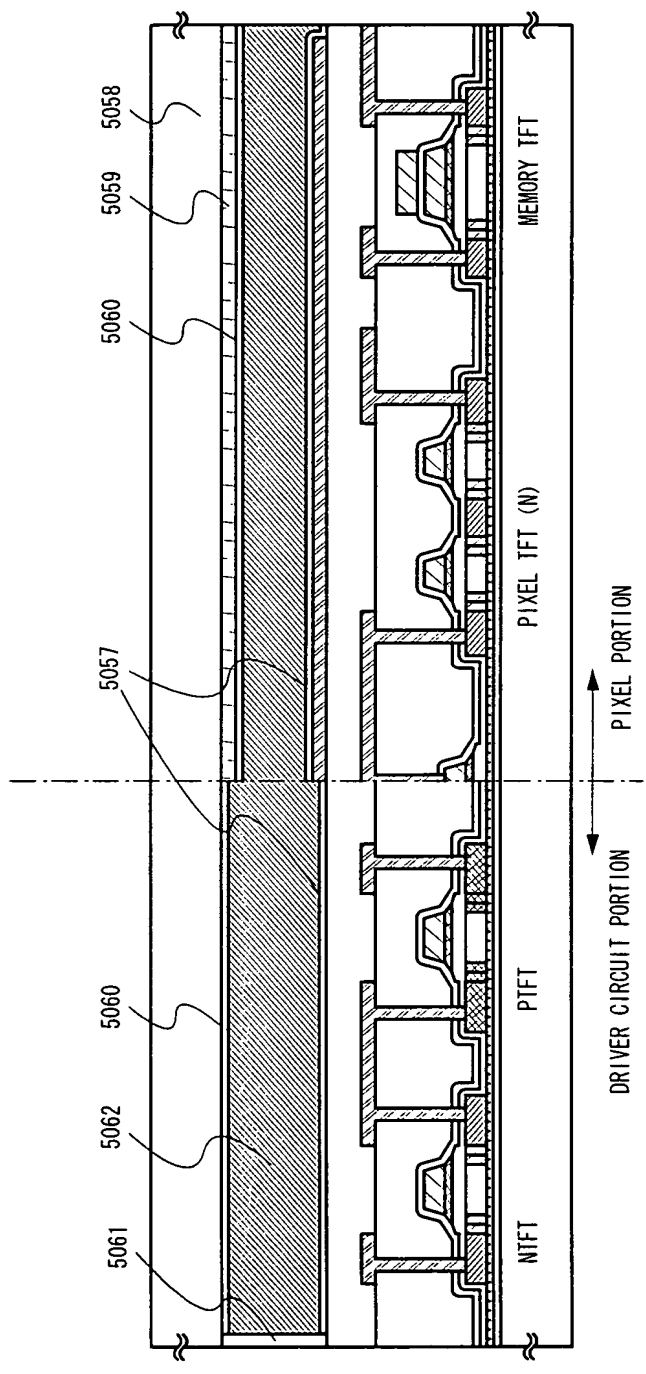


FIG. 12

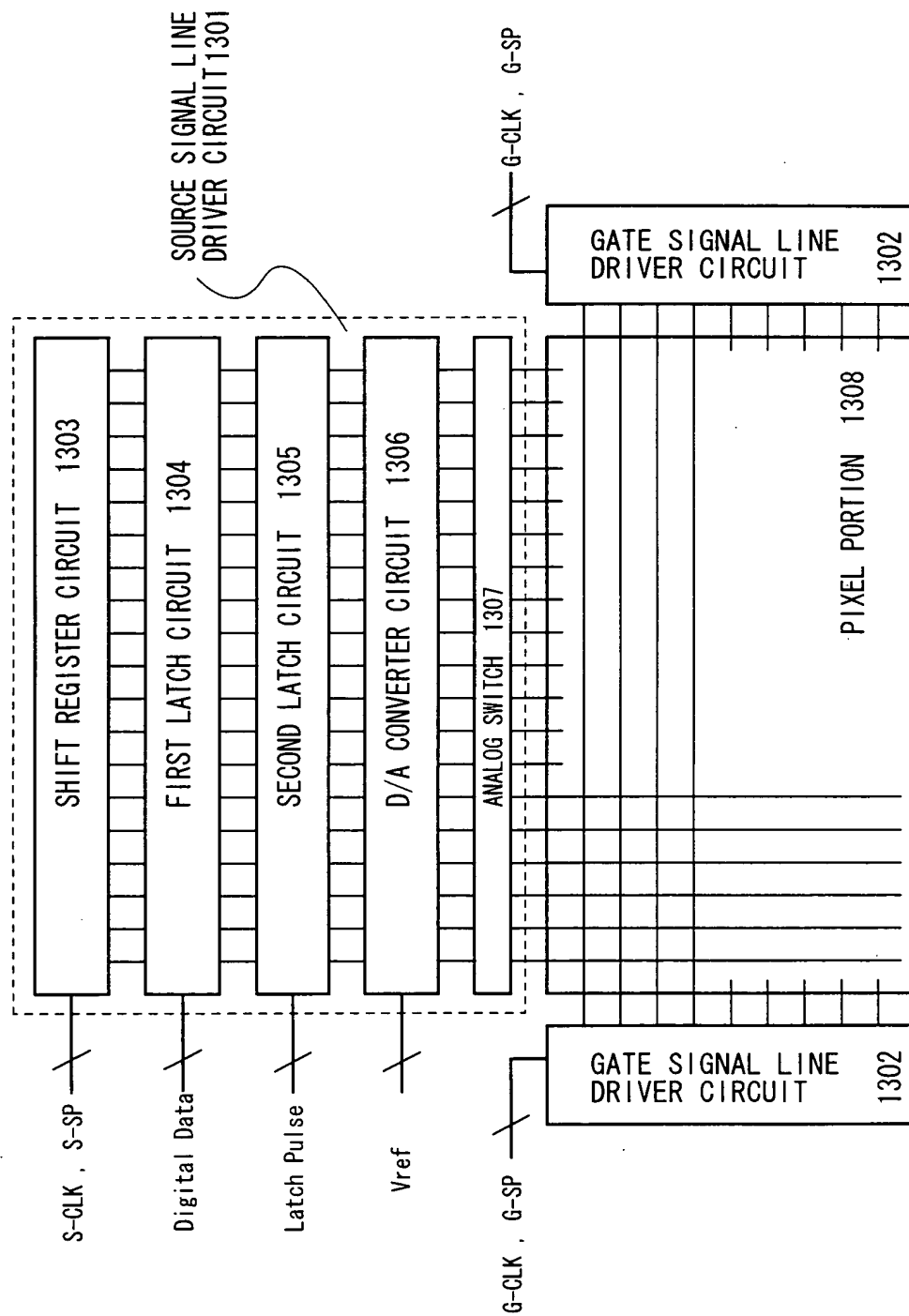


FIG. 13

1401
 1402
 1403
 1404

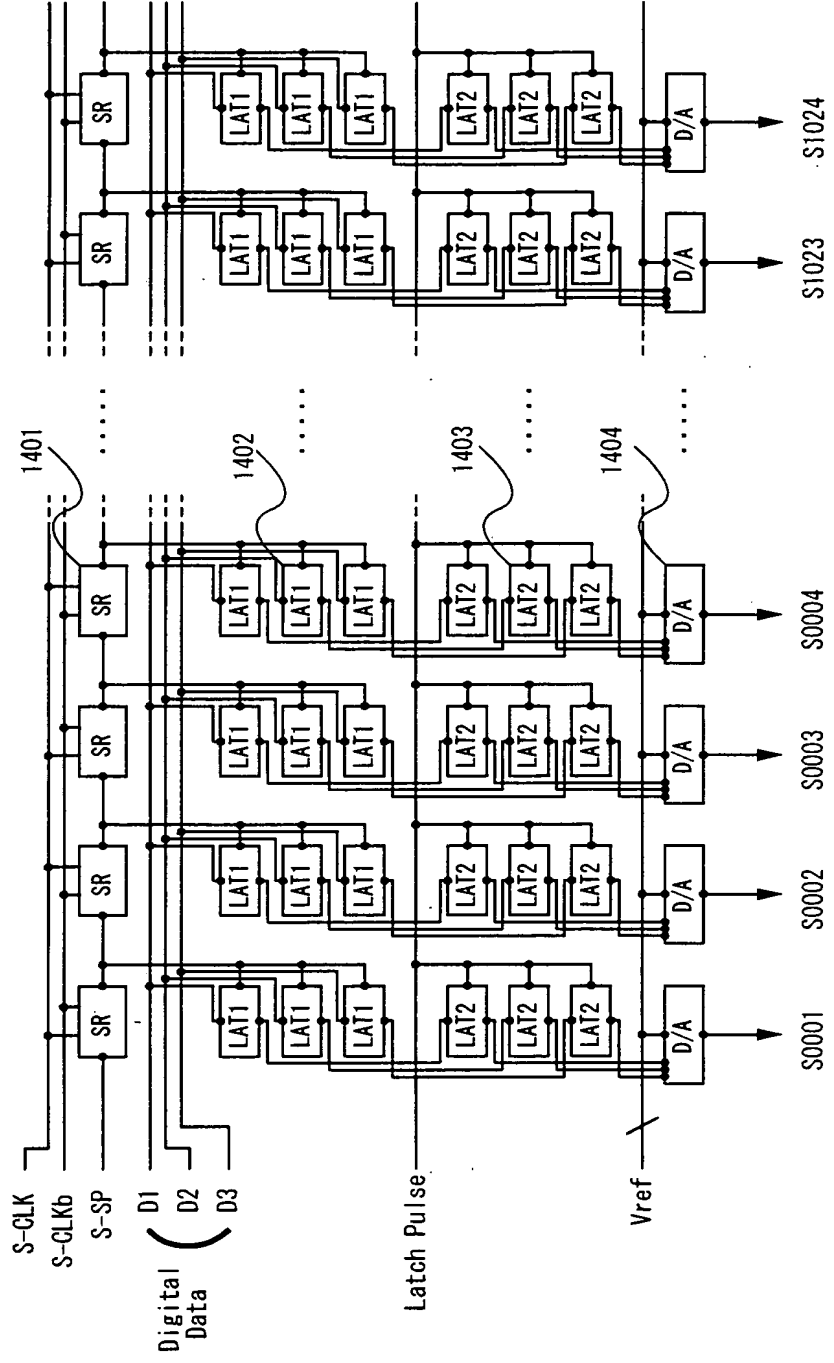


FIG. 14

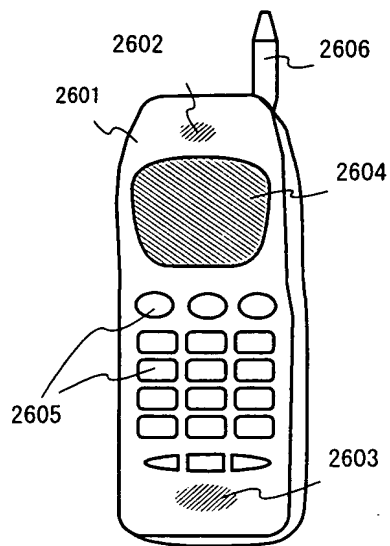


FIG. 15A

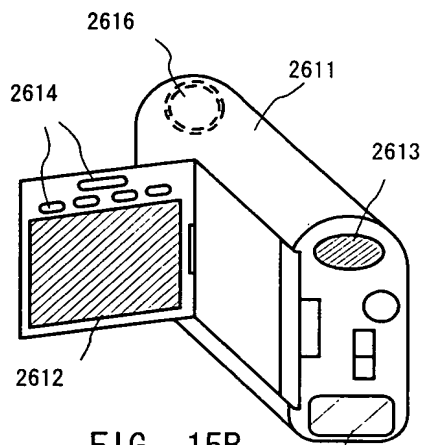


FIG. 15B

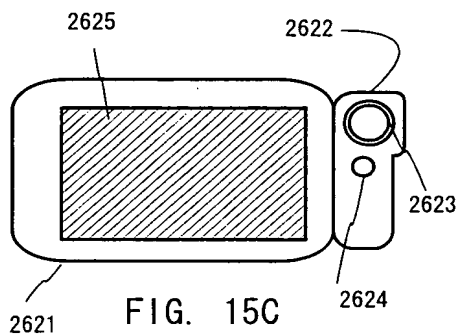


FIG. 15C

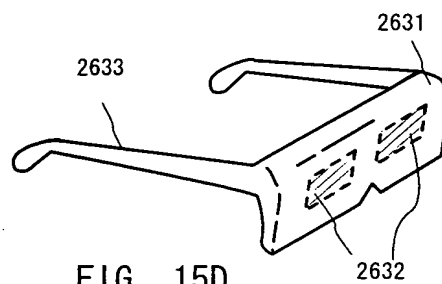


FIG. 15D

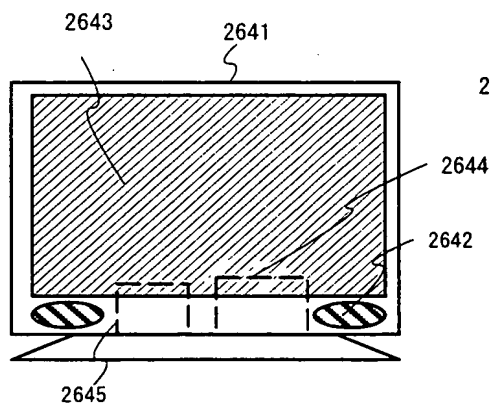


FIG. 15E

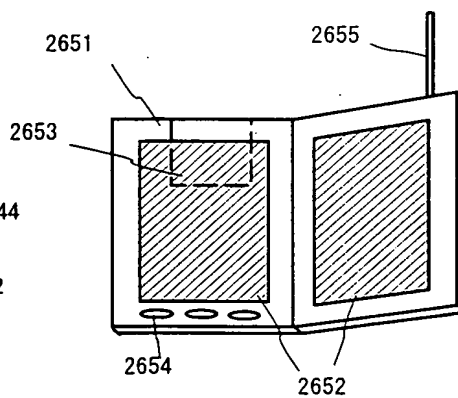


FIG. 15F

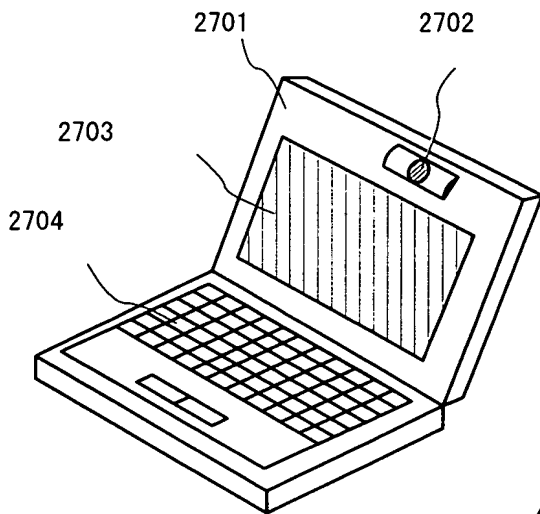


FIG. 16A

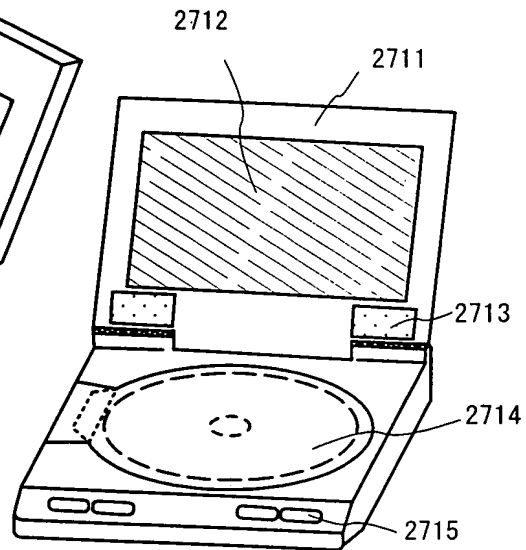


FIG. 16B

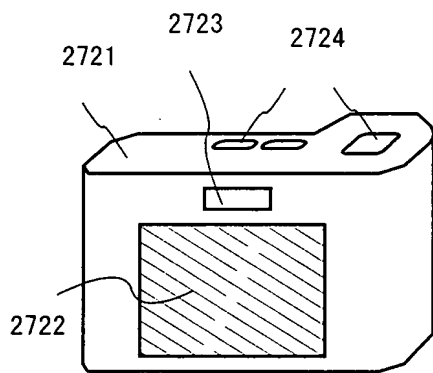


FIG. 16C

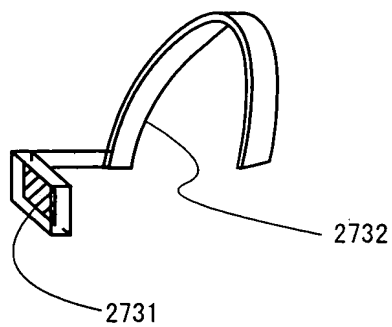


FIG. 16D

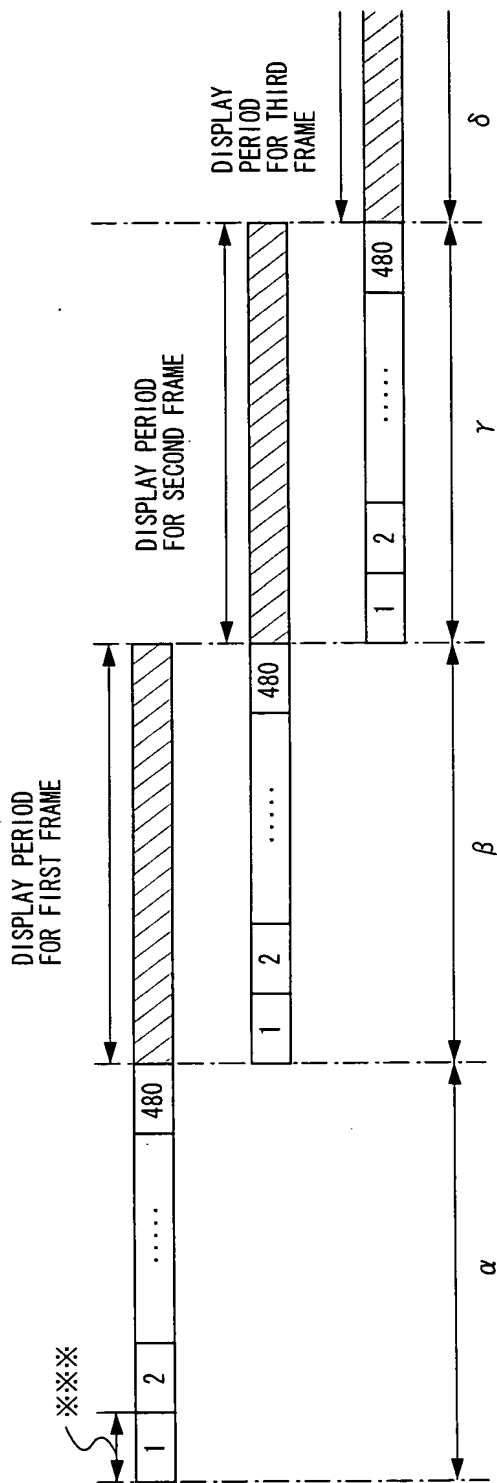


FIG. 18A

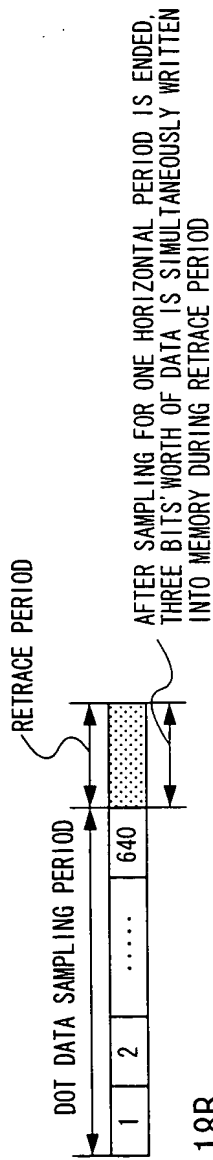


FIG. 18B

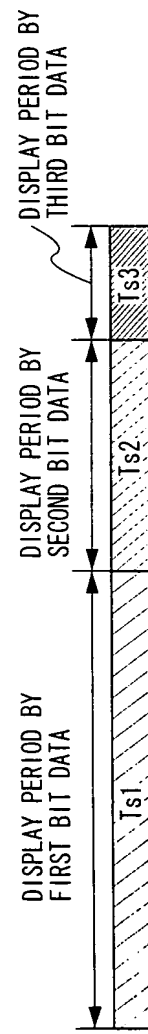


FIG. 18C

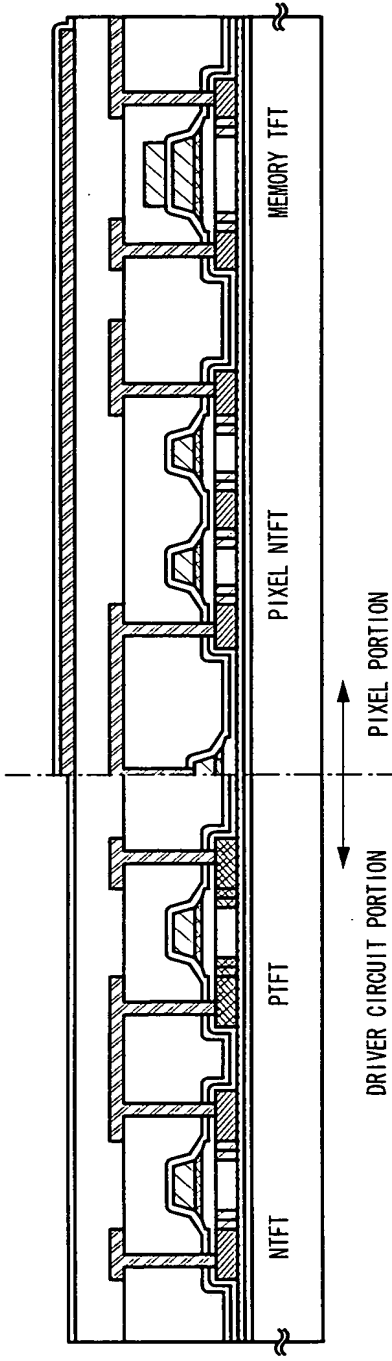


FIG. 19A

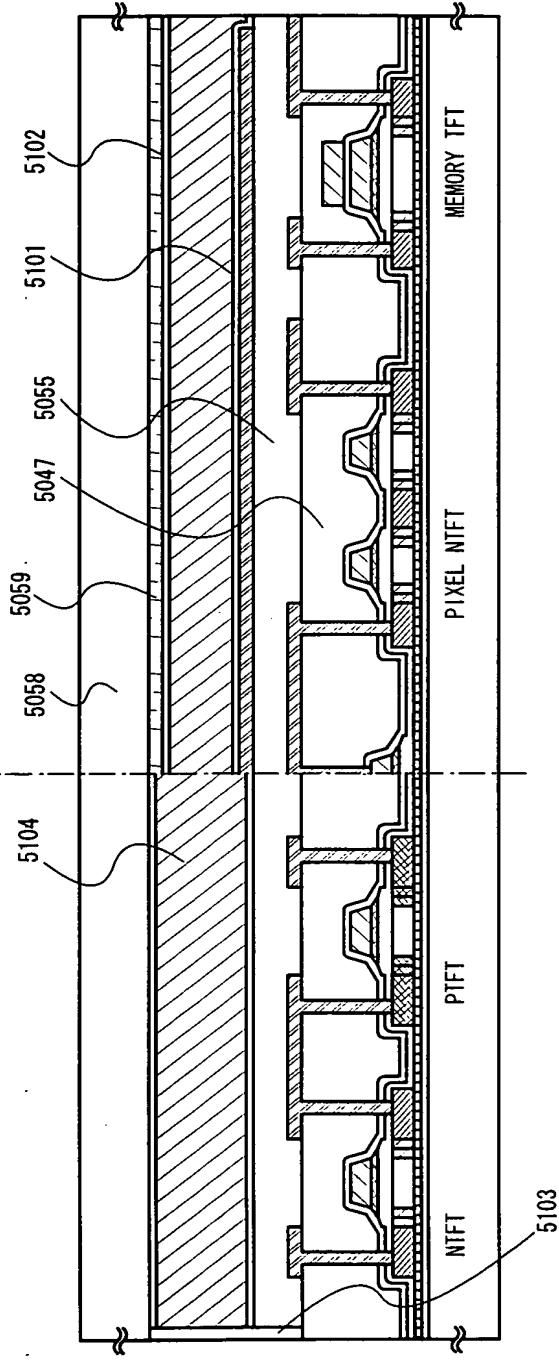


FIG. 19B

ADDRESS LINE

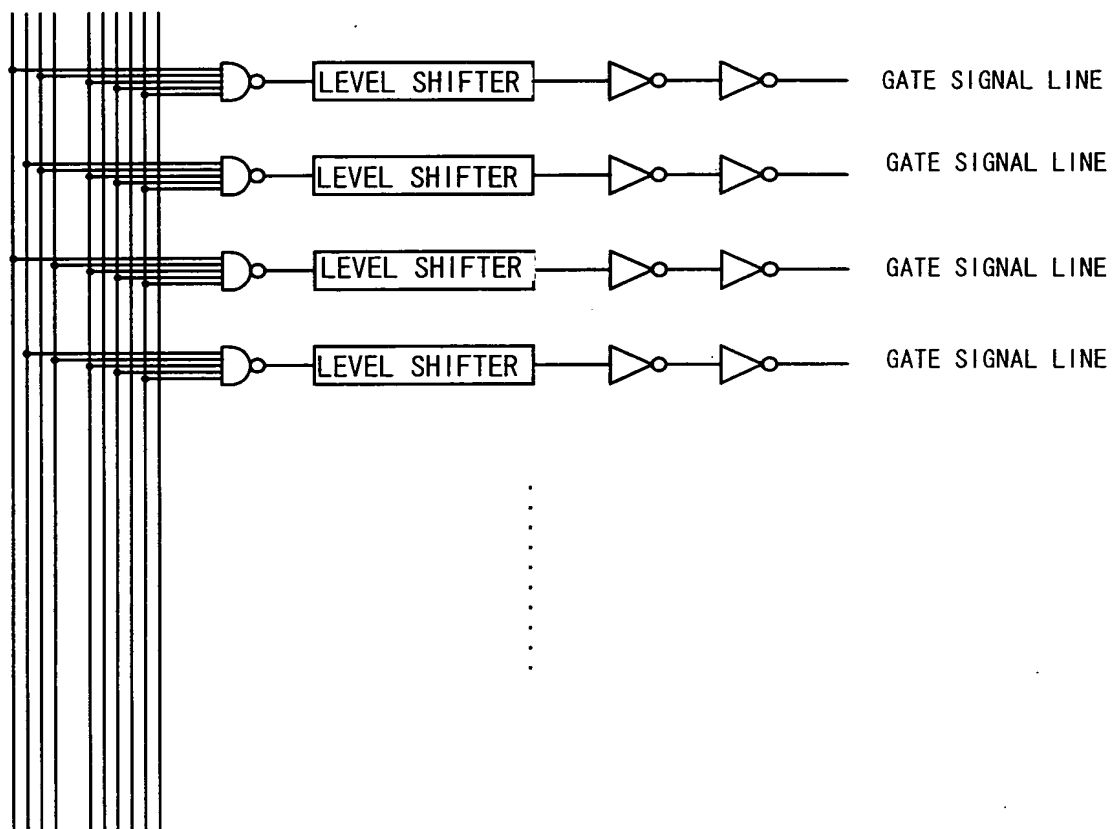


FIG. 20

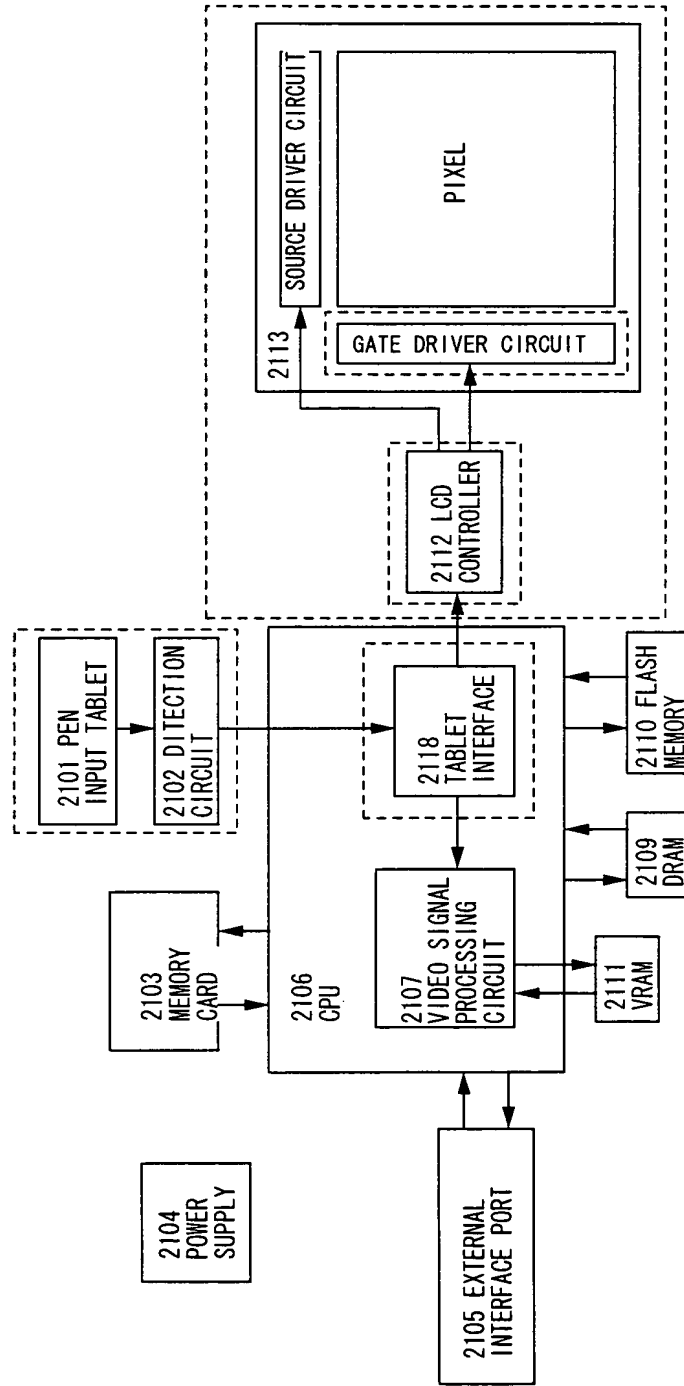


FIG. 21

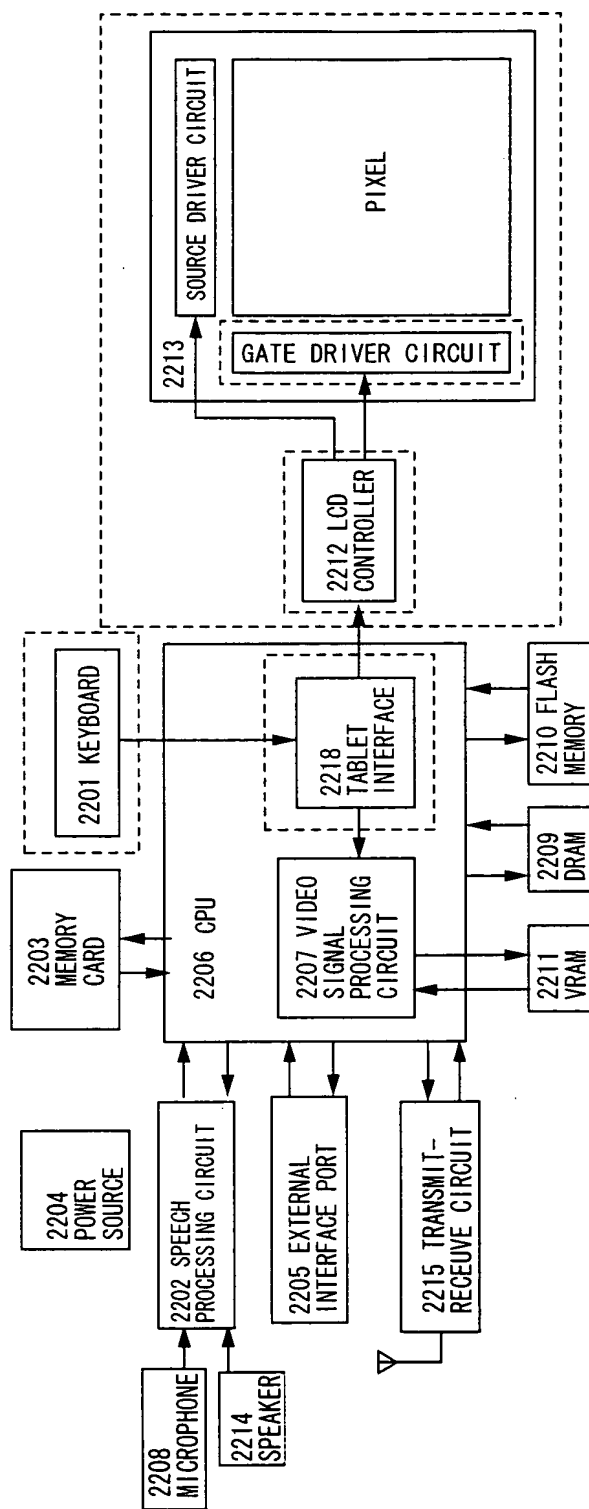


FIG. 22

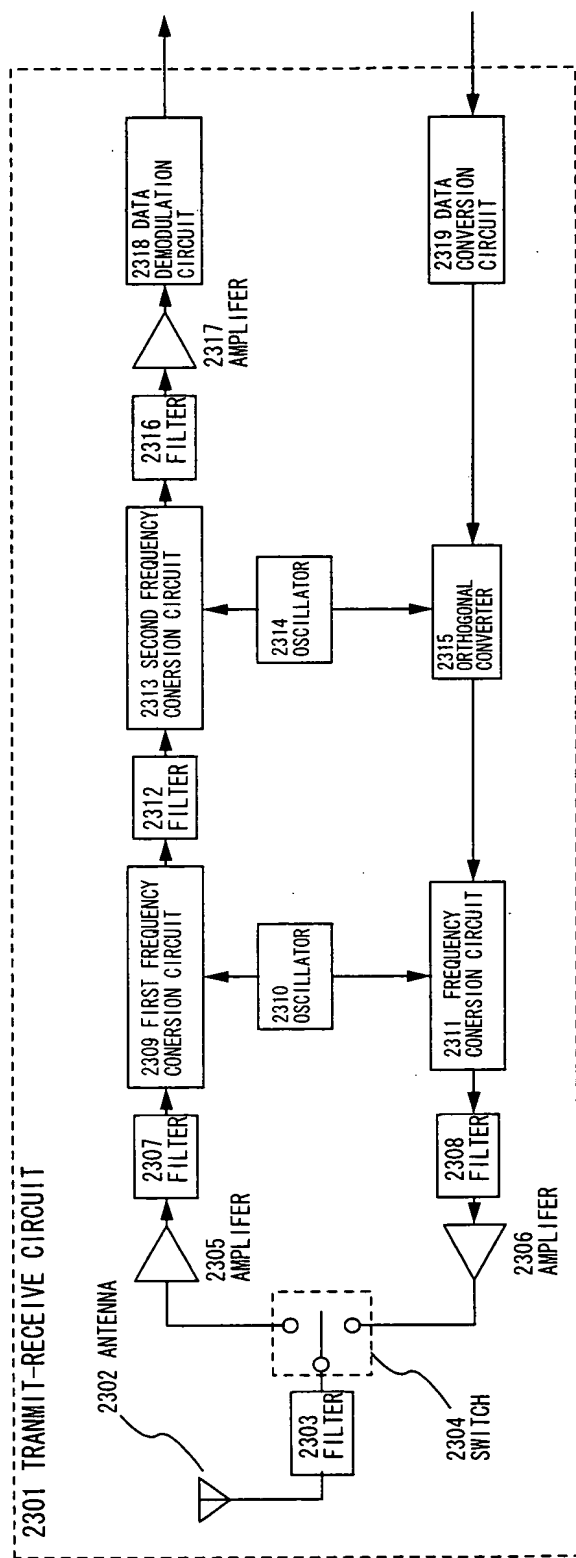


FIG. 23